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の回路図である。

【図6】

従来の保護回路及びその保護回路が適用される回路構造の回路図である。

【図7】

端における負荷の電源オフ時のサージ電流等の様子を示す図であ

明

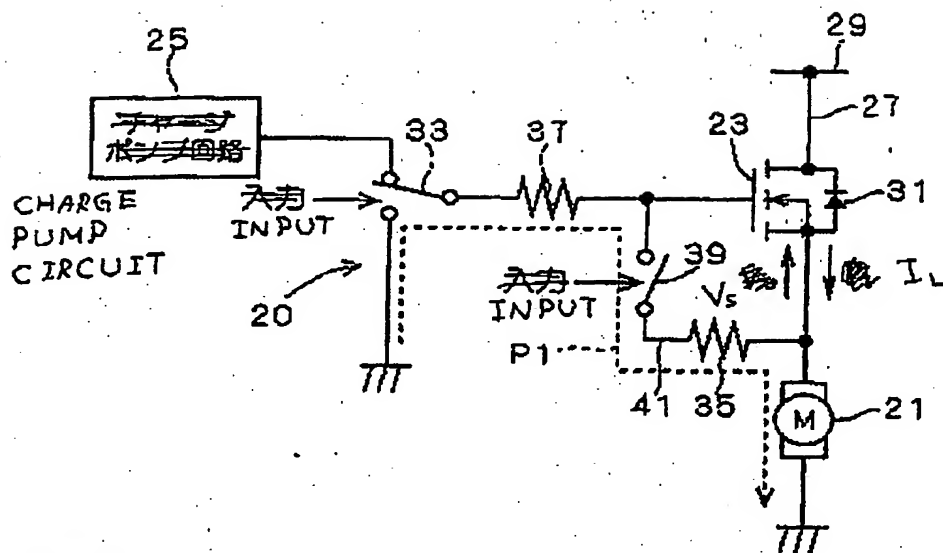
サージ電流

H5 1-167124 M/KT

~167125

US. DE

図面  
Fig. 1



【図2】

FIG. 2

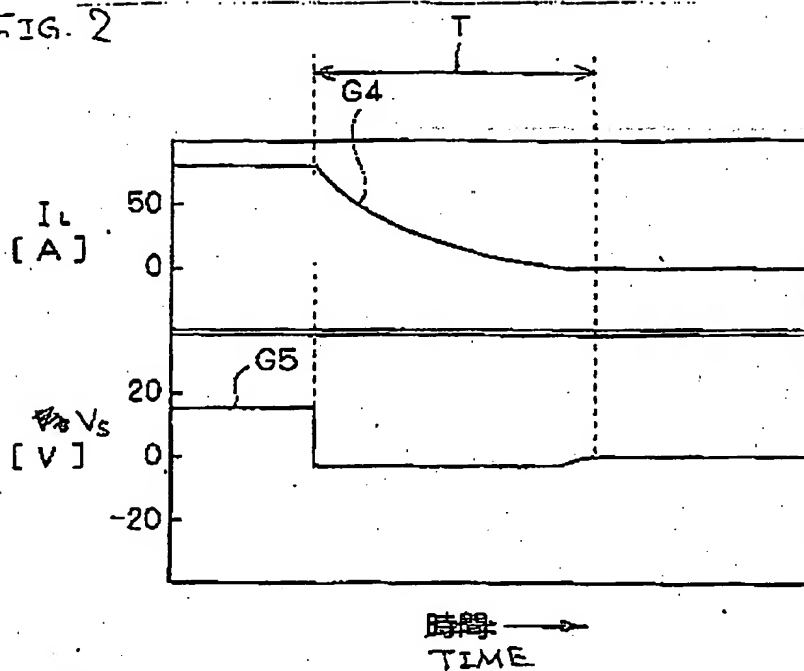


FIG. 3

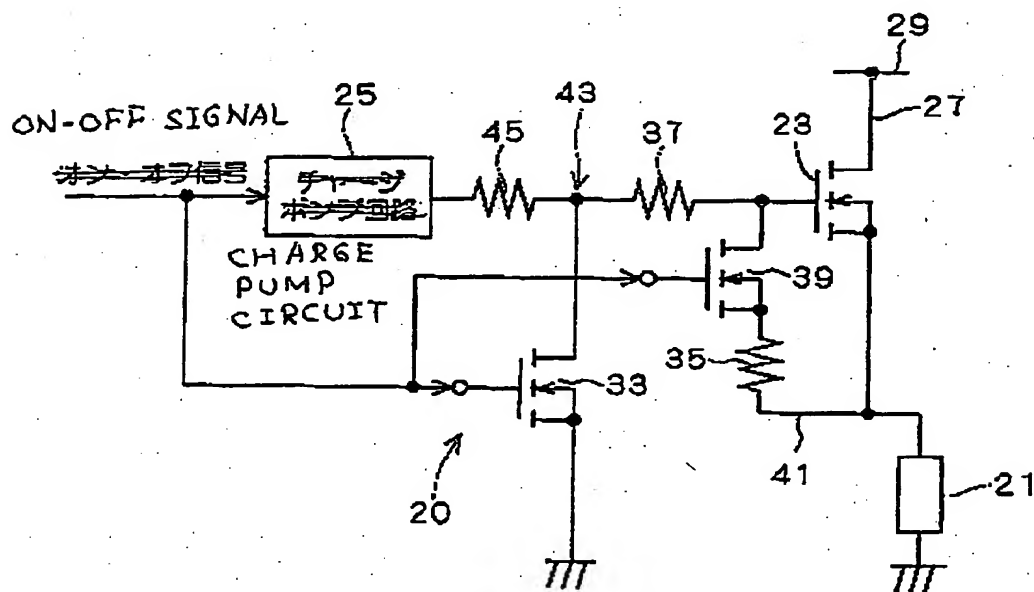


FIG. 4

FIG. 4

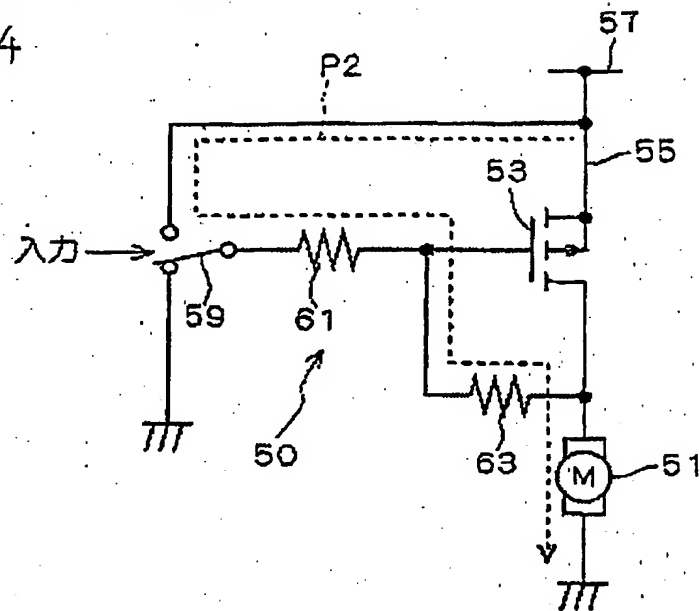


FIG. 5

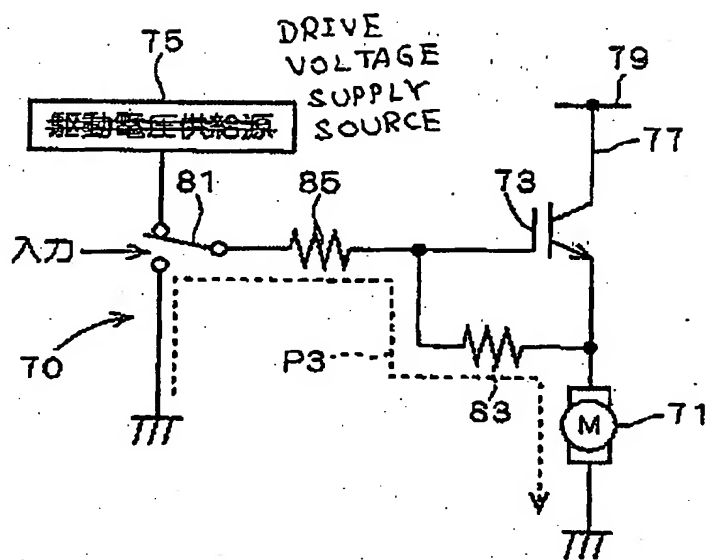


FIG. 6

FIG. 6

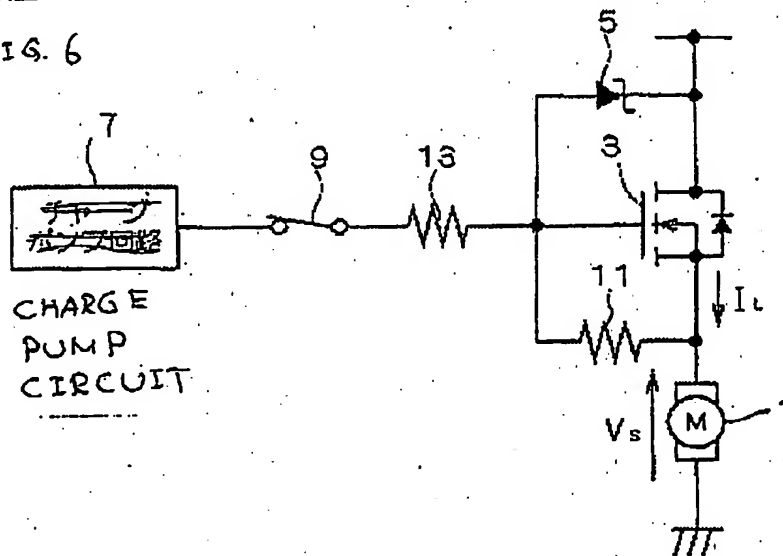
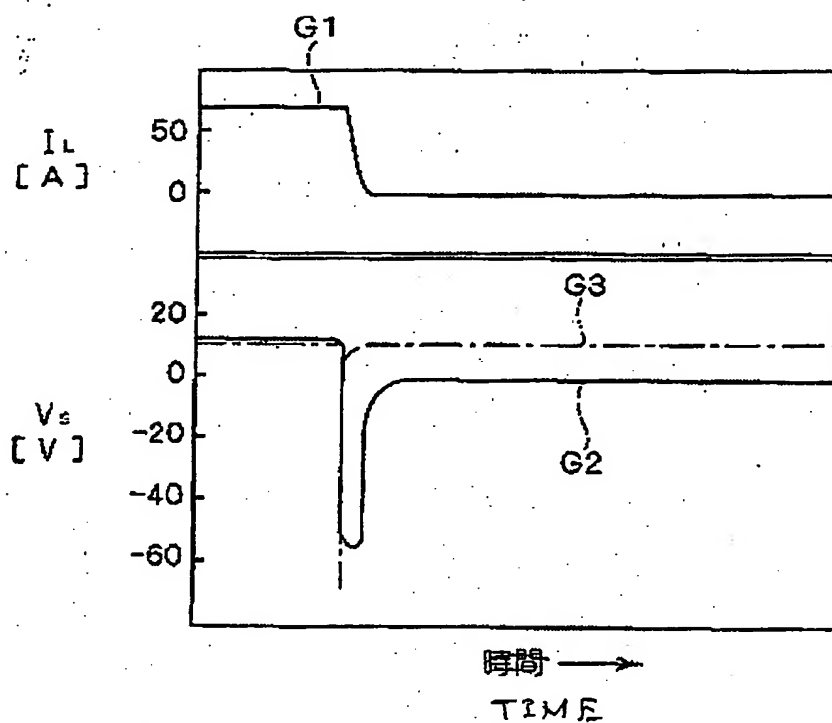


FIG 7



【要約】 要約文